

FIG. 1

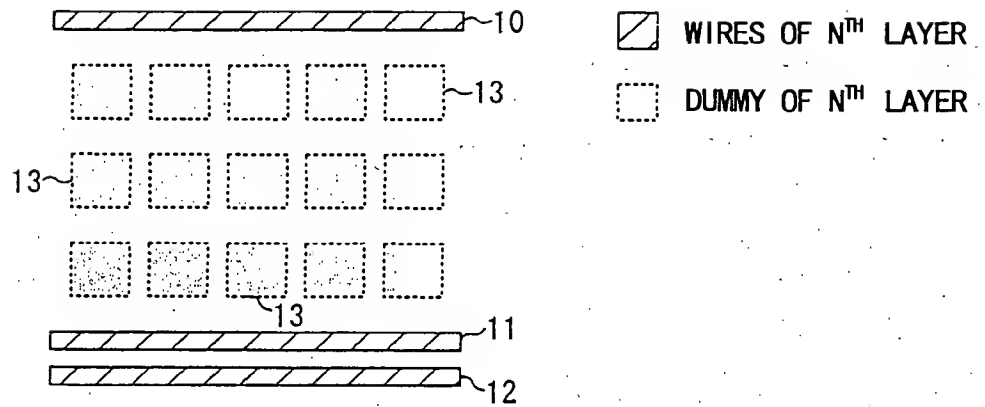


FIG. 2

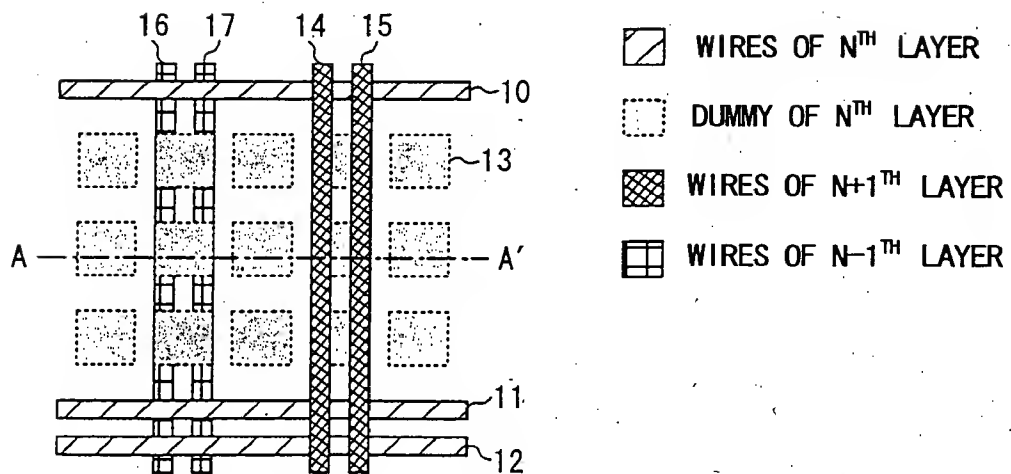


FIG. 3

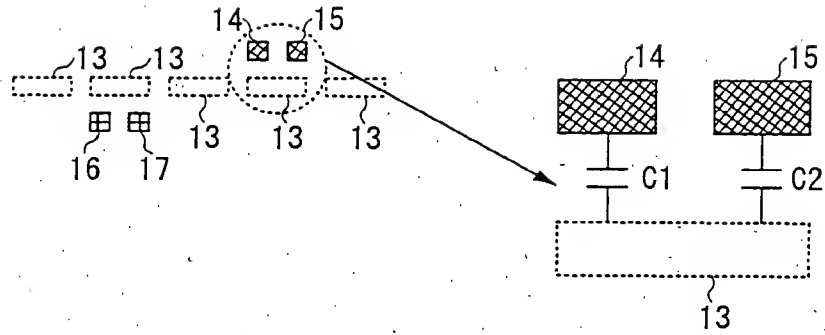
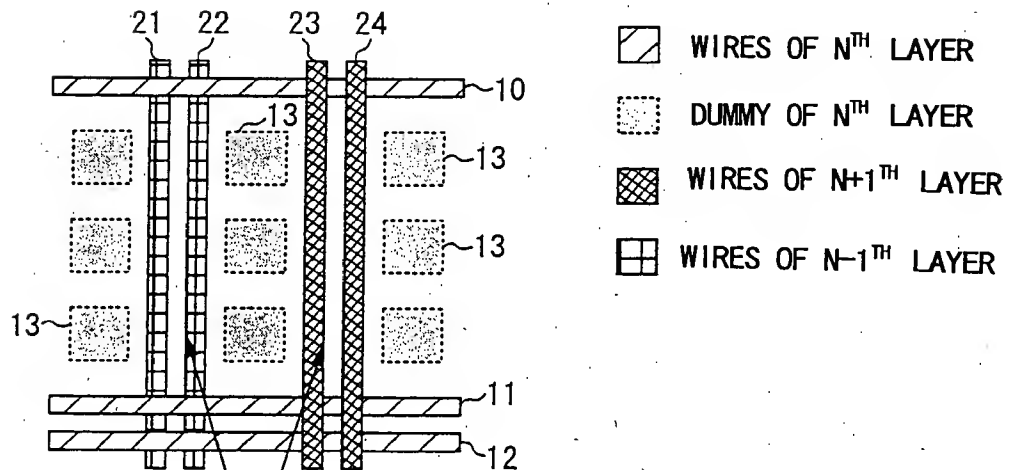


FIG. 4



NO DUMMY IN N^{TH} LAYER OVER AND UNDER WIRES OF $N+1^{\text{TH}}$ LAYER AND WIRES OF $N-1^{\text{TH}}$ LAYER

FIG. 5

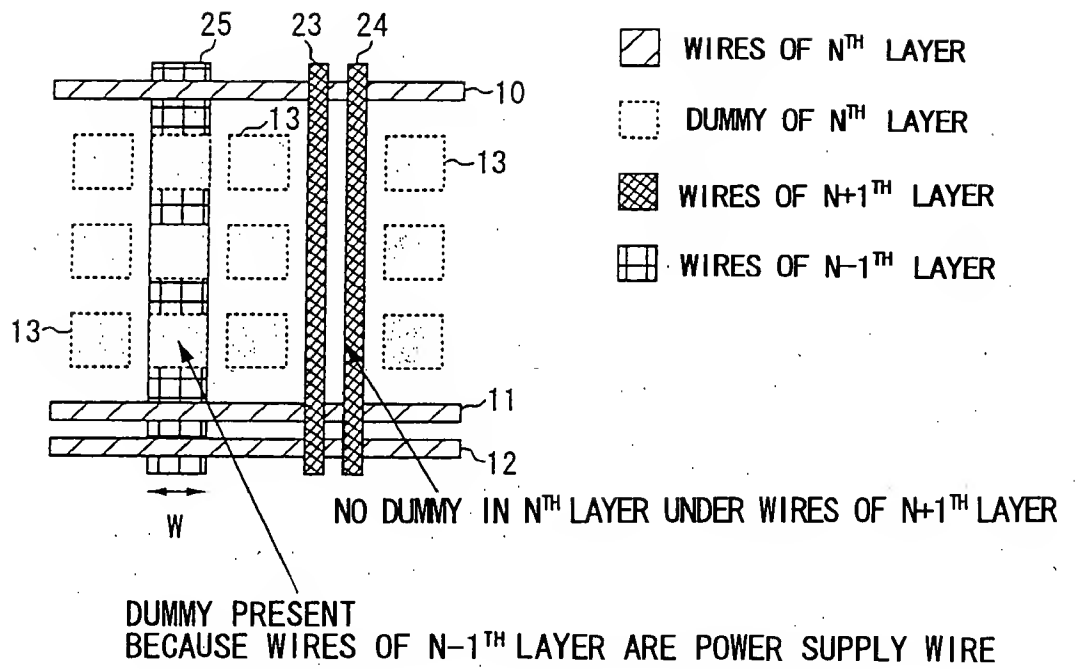


FIG. 6

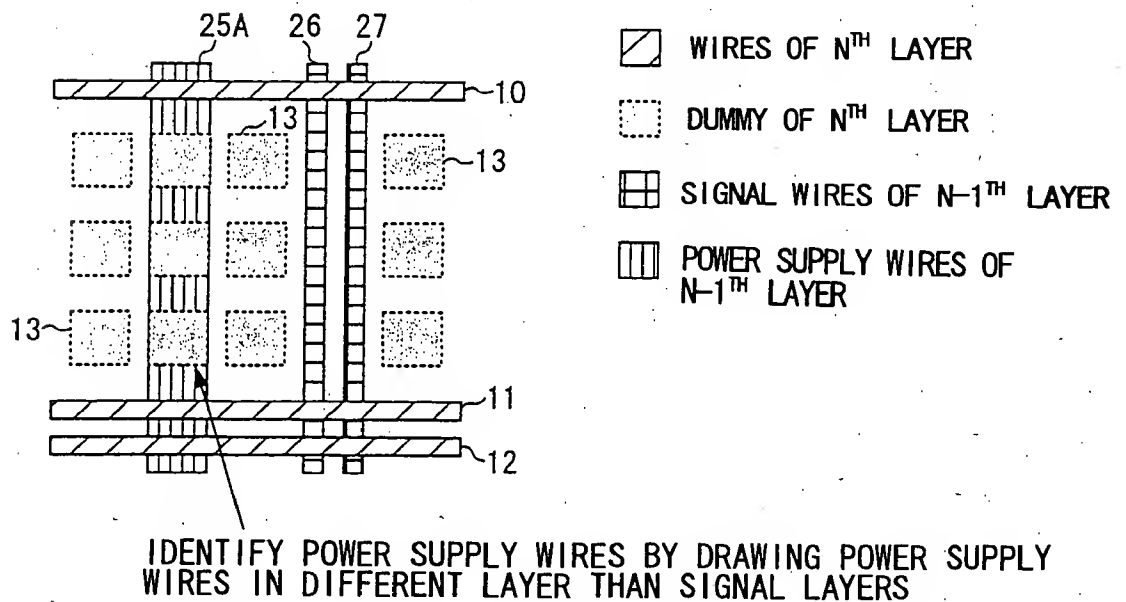
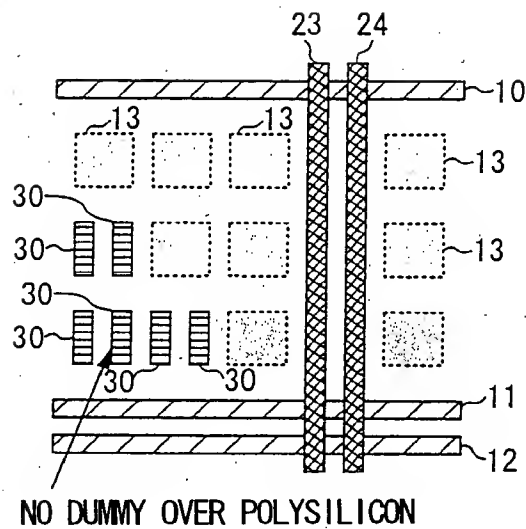
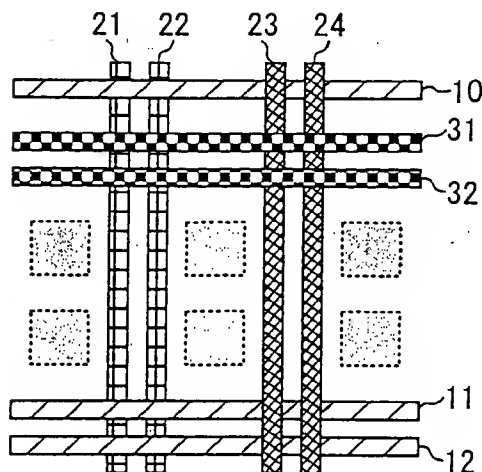


FIG. 7



- WIRES OF N^{TH} LAYER
- DUMMY OF N^{TH} LAYER
- WIRES OF $N+1^{\text{TH}}$ LAYER
- POLYSILICON OR DIFFUSION LAYER ($N-1^{\text{TH}}$ LAYER)

FIG. 8



- WIRES OF N^{TH} LAYER
- DUMMY OF N^{TH} LAYER
- WIRES OF $N+1^{\text{TH}}$ LAYER
- WIRES OF $N+M^{\text{TH}}$ LAYER (M IS POSITIVE INTEGER)
- WIRES OF $N-1^{\text{TH}}$ LAYER

FIG. 9A

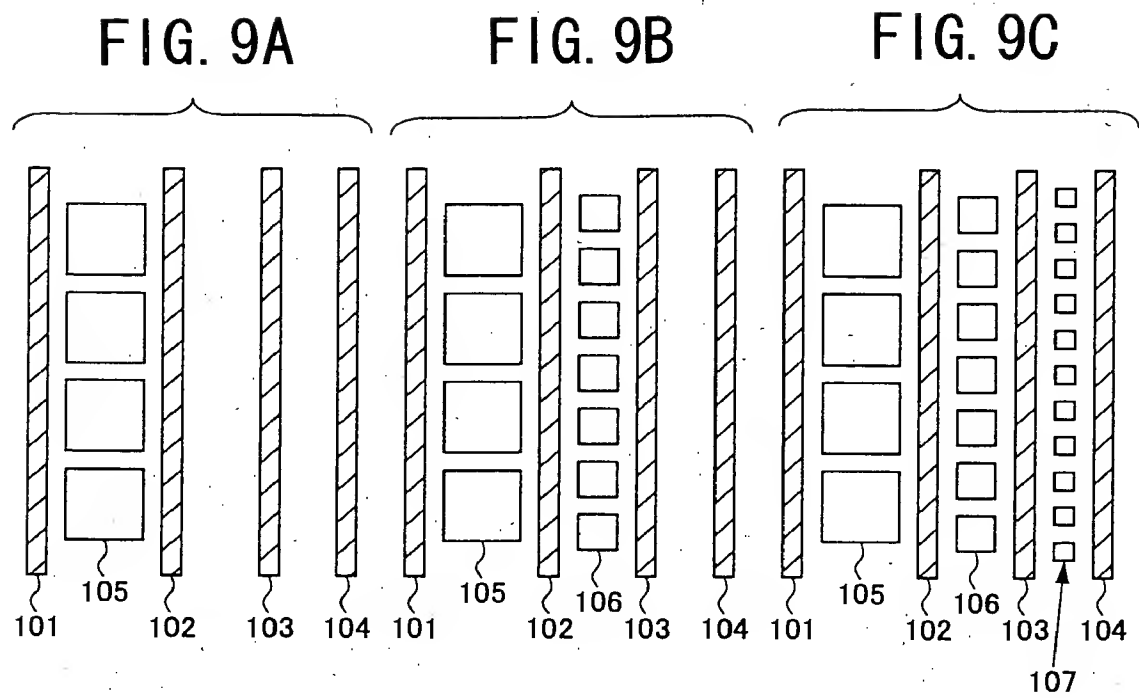


FIG. 10

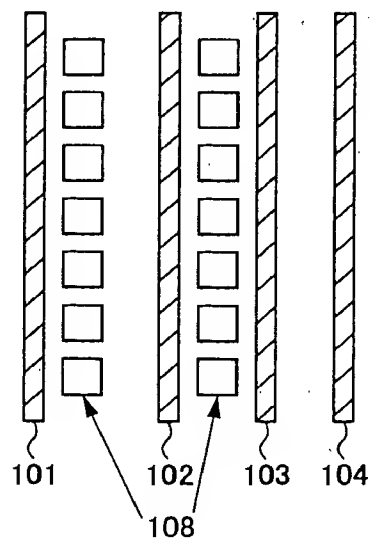


FIG. 14A

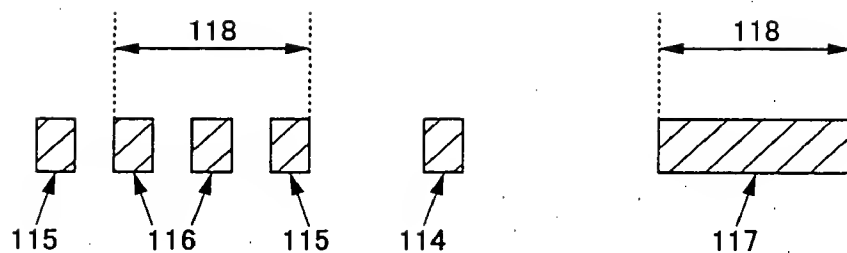
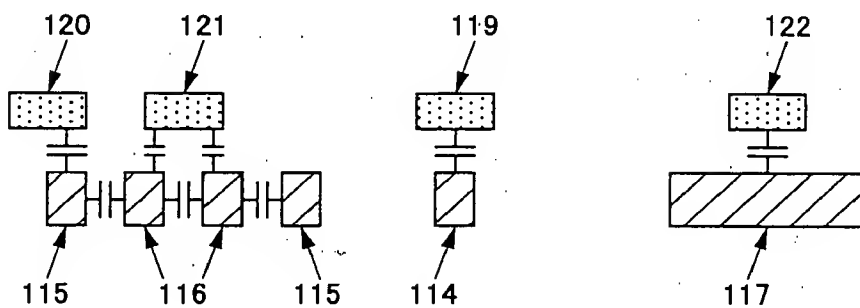


FIG. 14B



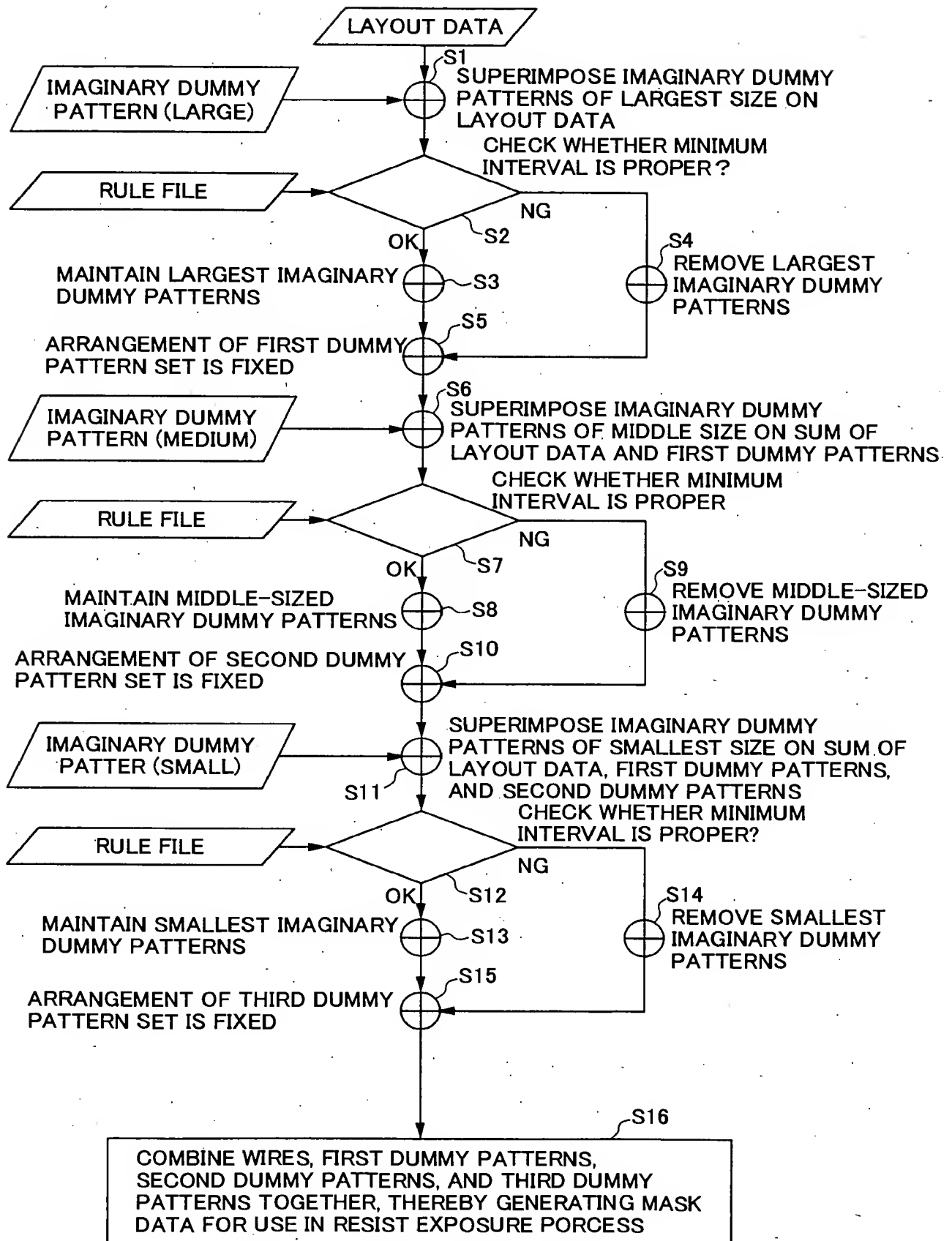
[illegible]

FIG. 12A

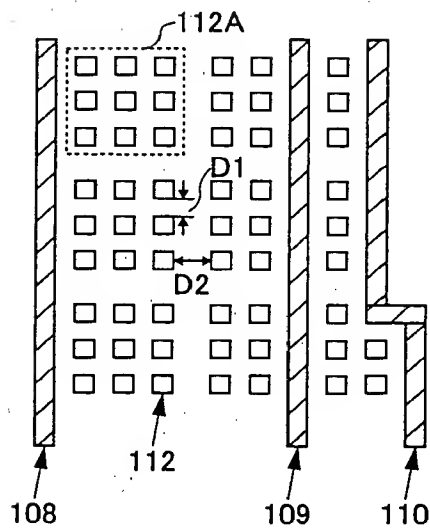


FIG. 12B

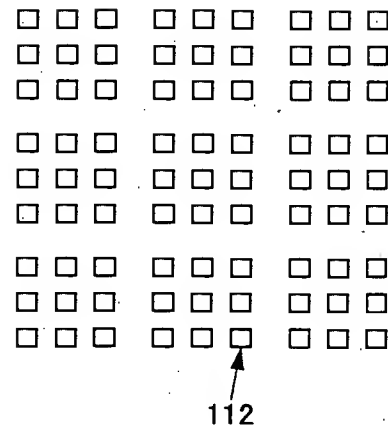


FIG. 12C

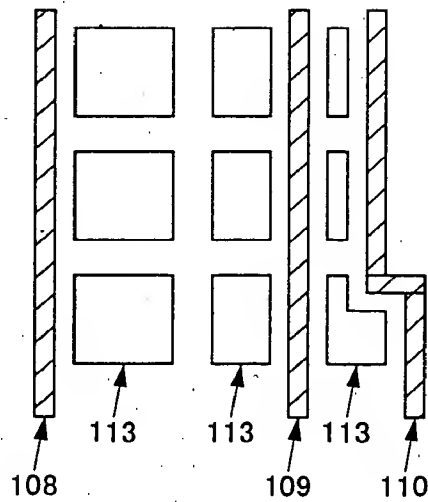


FIG.13

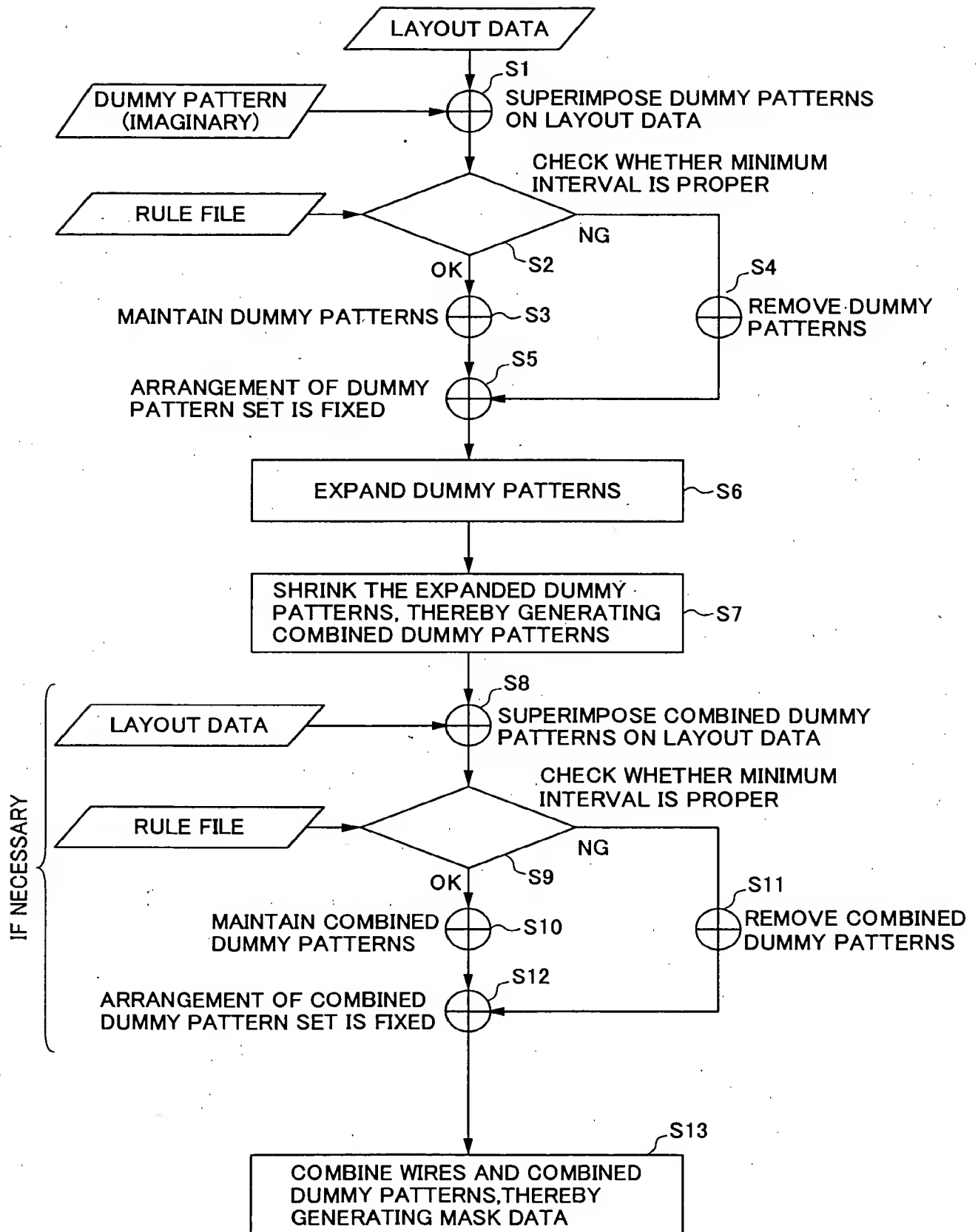


FIG.15

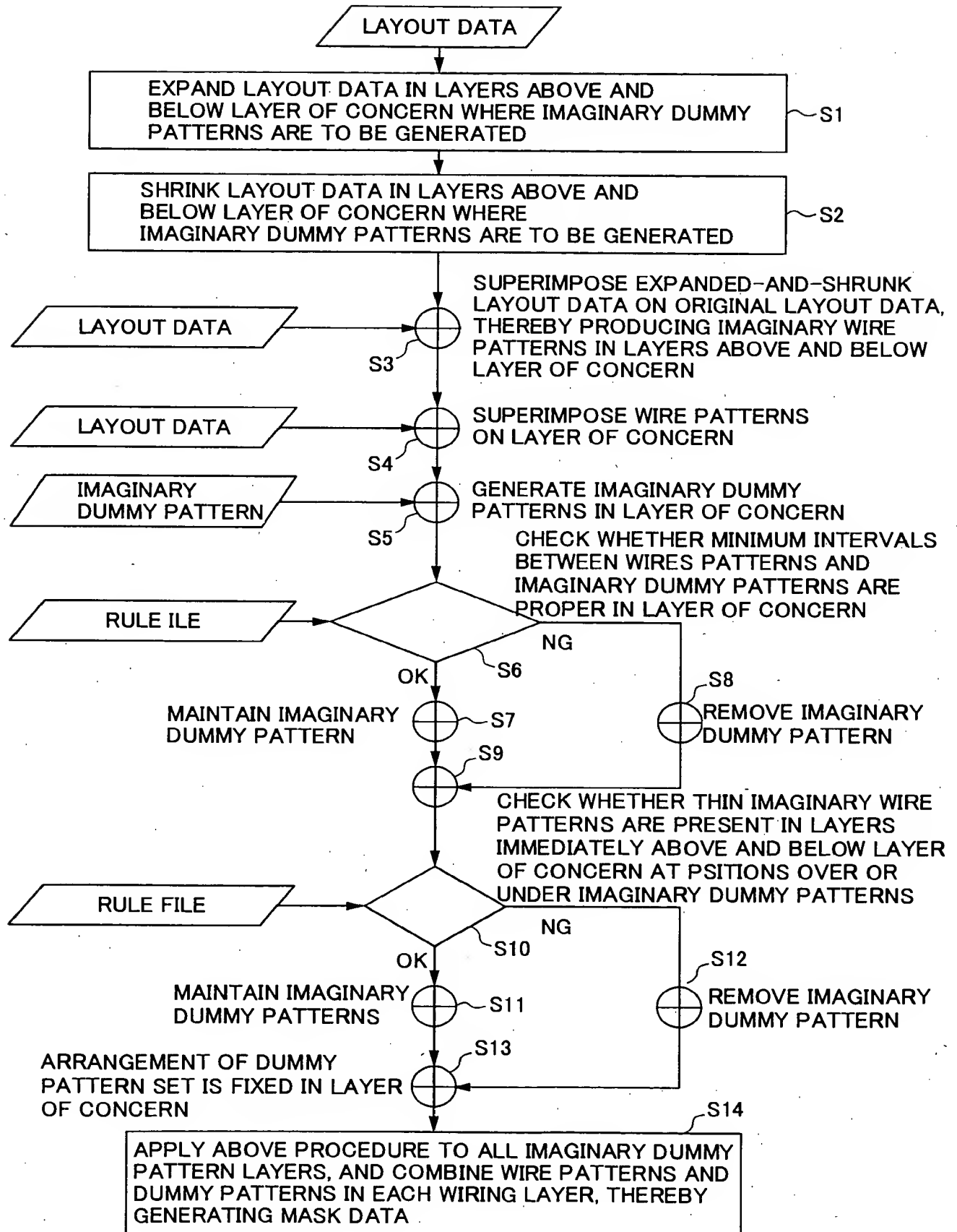


FIG. 15

FIG. 16

INITIAL WIRES

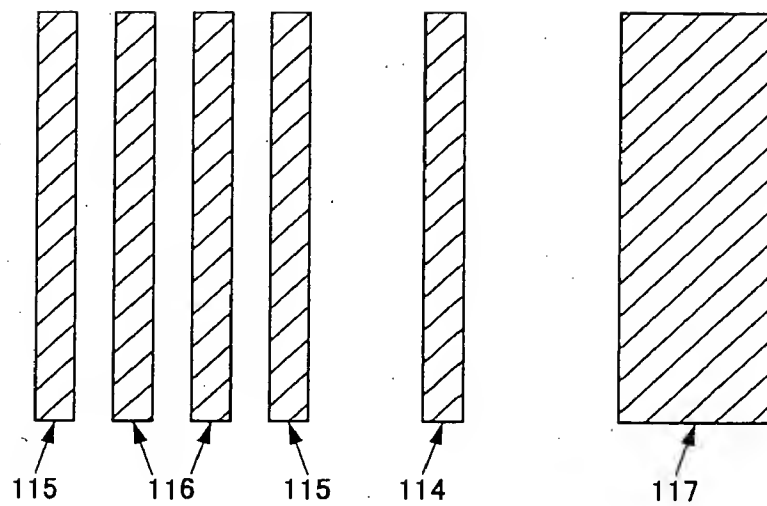


FIG. 17

FIRST PROCESS

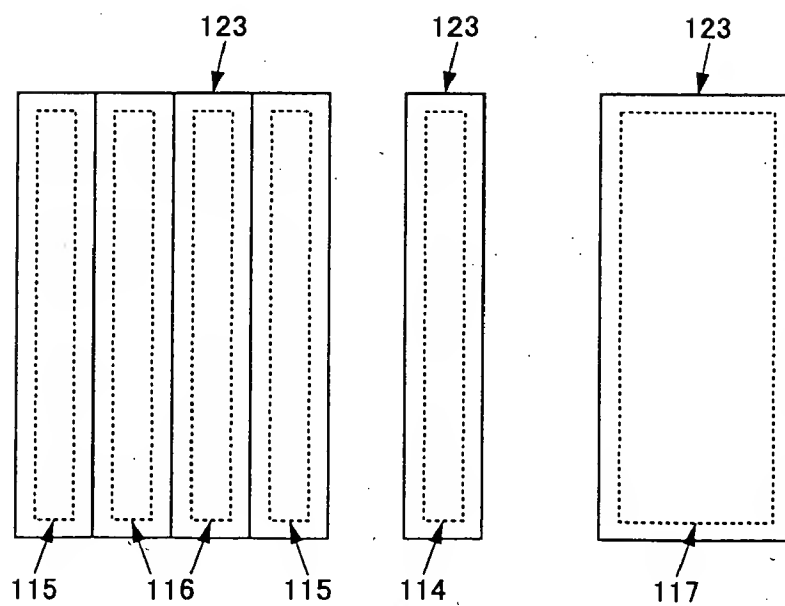


FIG. 18

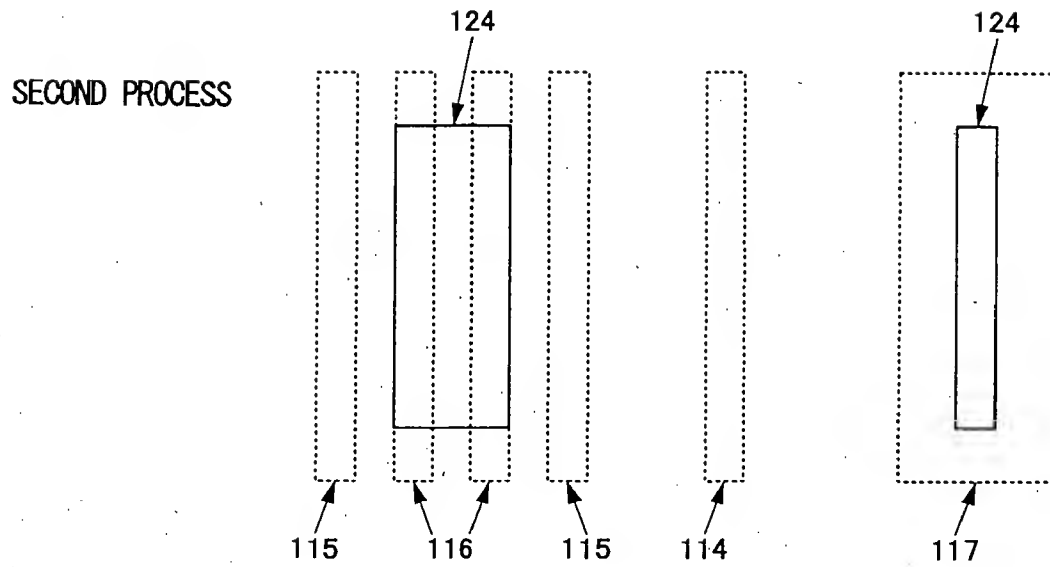


FIG. 19

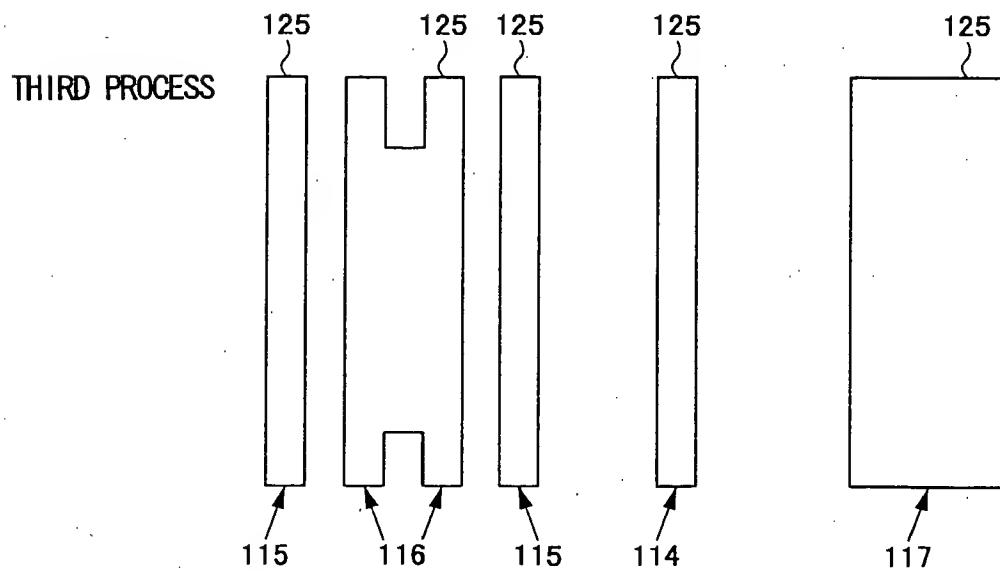


FIG. 20

FOURTH PROCESS

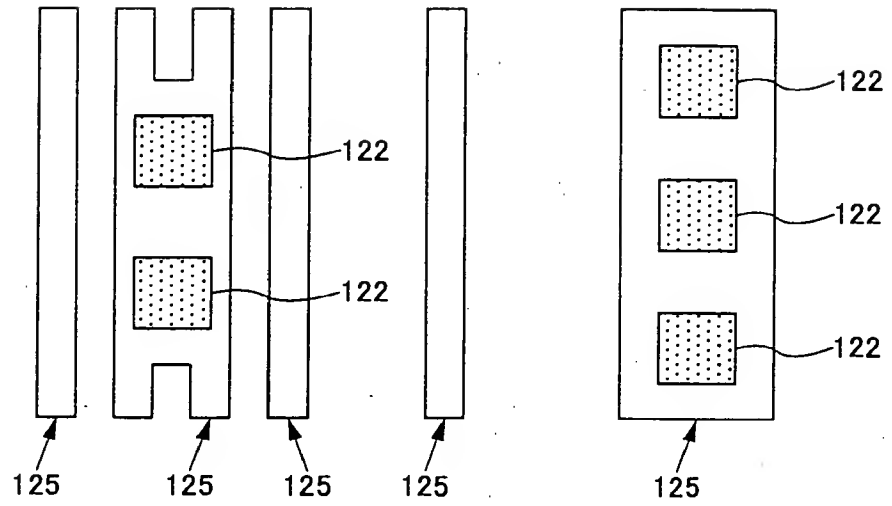


FIG. 21

FIFTH PROCESS

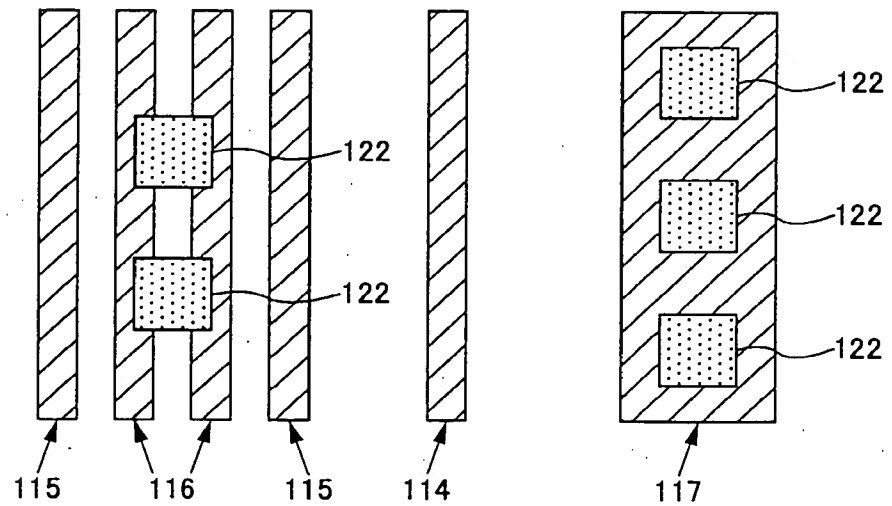


FIG. 22

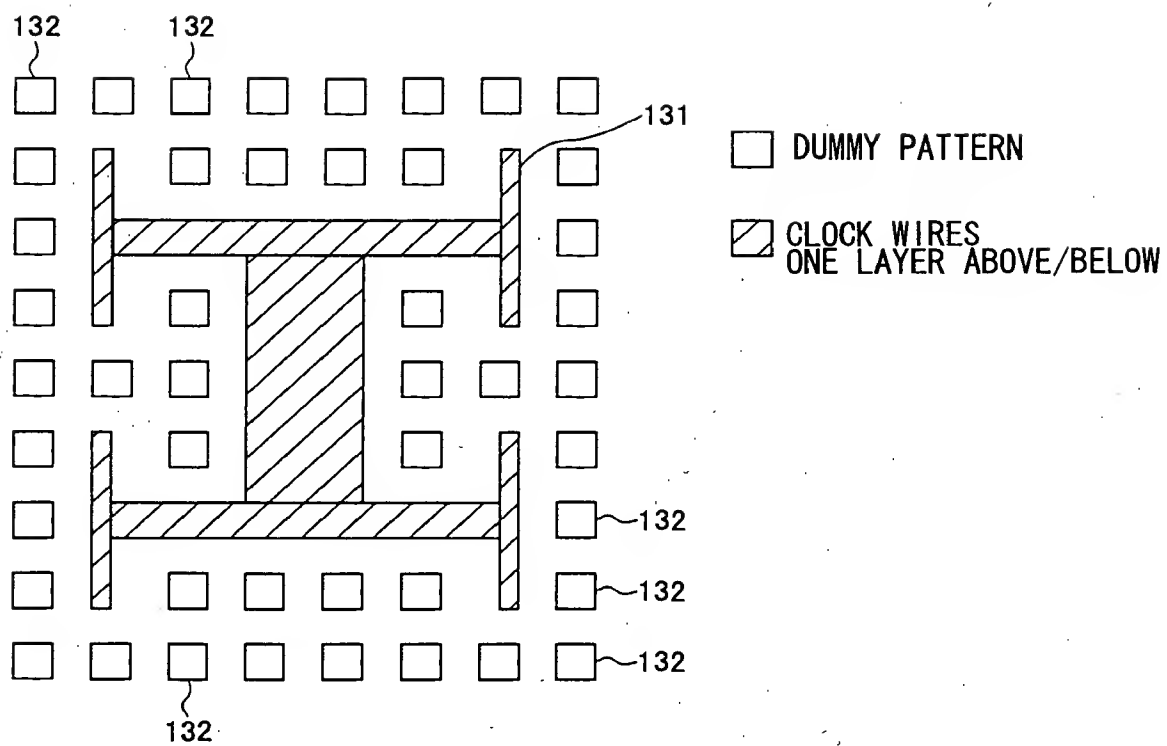


FIG.23

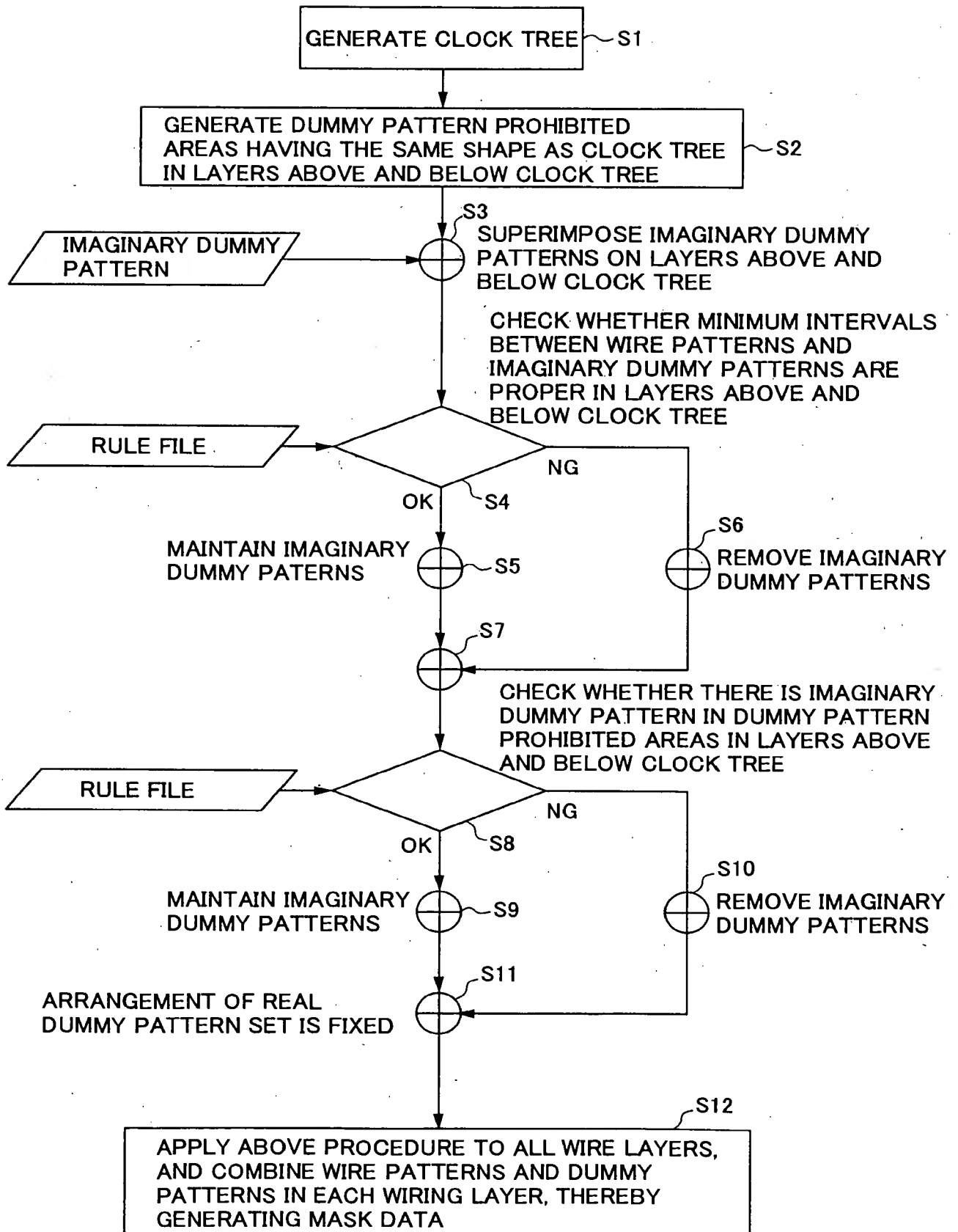


FIG. 24

